

Application No.: 10/724,277

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Docket No.: 188122001500

**AMENDMENTS TO THE SPECIFICATION**

Please Amend ¶ [0001] of the specification as follows.

[0001] This application is related to the following U.S. patent applications, each of which is hereby incorporated by reference in its entirety: System And Method For Dynamically Compressing Circuit Components During Simulation, Serial No. 10/713,729, filed November 13, 2003; attorney docket number 010585-0016-999; System And Method For Adaptive Partitioning Of Circuit Components During Simulation, Serial No. 10/713,751, filed November 13, 2003; attorney docket number 010585-0017-999; System And Method For Communicating Simulation Solutions Between Circuit Components In A Hierarchical Data Structure, Serial No. 10/713,754, filed November 13, 2003; attorney docket number 010585-0018-999; System And Method For Supporting Multi-rate Simulation Of A Circuit Having Hierarchical Data Structure, Serial No. 10/713,753, filed November 13, 2003; attorney docket number 010585-0019-999; and System And Method For Dynamically Representing Repetitive Loads Of A Circuit During Simulation, Serial No. 10/713,728, filed November 13, 2003; attorney docket number 010585-0020-999.

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